

WHAT IS CLAIMED IS:

1. A method of printing multiple images with differing line screen frequencies with a printer is print sensitive to differing line screen frequencies, the method  
5 comprising the steps of:

converting the images into a digital bitmap comprised of an array of pixels wherein each pixel is assigned a digital value representing marking information;

defining each pixel as either a background pixel, interior pixel, or an edge pixel;

reassigning the digital value of the interior pixels to a fixed interior pixel value;

10 and

reassigning the edge pixel values so as to minimize print sensitivity to differing line screen frequencies between images.

2. A method in accordance with claim 1, wherein the converting step comprises  
15 converting the image to a binary digital bitmap and the reassigning step comprises reassigning the binary digital values to multi-bit digital values.

3. A method in accordance with claim 1, wherein the converting step comprises  
converting the image to a multi-bit digital bitmap and the reassigning step comprises  
20 reassigning the binary digital values to multi-bit digital values.

4. A method in accordance with claim 1, wherein the reassigning step comprises increasing the value of edge pixels with respect to interior pixels.

5. A method in accordance with claim 1, wherein the reassigning step comprises decreasing the value of edge pixels with respect to interior pixels.

5           6. A method in accordance with claim 1, further comprising performing the defining and reassigning steps two or more times.

7. A method in accordance with claim 1, wherein the reassigning step comprises reassigning multiple interior pixel values.

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8. A method of printing multiple images with differing line screen frequencies with a printer is print sensitive to differing line screen frequencies, the method comprising the steps of:

15           converting the images into a digital bitmap comprised of an array of pixels wherein each pixel is assigned a digital value representing marking information;

          defining each pixel as either a background pixel, interior pixel, edge pixel, one line pixel, or two line pixel; or an edge pixel;

          reassigning the digital value of the interior pixels to a fixed interior pixel value; and,

20           reassigning one or more of the edge pixel values, one line pixel values, or two line pixel values independently so as to minimize print sensitivity to differing line screen frequencies between images.

9. A method in accordance with claim 8, wherein the converting step comprises converting the image to a binary digital bitmap and the reassigning step comprises reassigning the binary digital values to multi-bit digital values.

5           10. A method in accordance with claim 8, wherein the converting step comprises converting the image to a multi-bit digital bitmap and the reassigning step comprises reassigning the binary digital values to multi-bit digital values.

10           11. A method in accordance with claim 8, wherein the reassigning step comprises increasing the value of edge pixels with respect to interior pixels.

12. A method in accordance with claim 8, wherein the reassigning step comprises decreasing the value of edge pixels with respect to interior pixels.

15           13. A method in accordance with claim 8, further comprising performing the defining and reassigning steps two or more times.

20           14. An apparatus for altering the appearance of multiple images with differing line screen frequencies printed with a printer which is print sensitive to differing line screen frequencies, apparatus comprising:

a raster image processor for converting the images into a digital bitmap comprised of an array of pixels wherein each pixel is assigned a digital value representing marking information;

a rendering circuit for:

defining each pixel as either a background pixel, interior pixel, or an edge pixel;

5 reassigning the digital value of the interior pixels to a fixed interior pixel value; and,

reassigning the edge pixel values so as to minimize print sensitivity to differing line screen frequencies between images.

10 15. An apparatus in accordance with claim 14, wherein the digital image data is binary.

16. An apparatus in accordance with claim 14, wherein the digital image data is a multi-bit.

15 17. An apparatus in accordance with claim 14, wherein reassigning comprises increasing the value of edge pixels with respect to interior pixels.

18. An apparatus in accordance with claim 14, wherein reassigning step comprises decreasing the value of edge pixels with respect to interior pixels.

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19. An apparatus in accordance with claim 14, wherein the rendering circuit further comprises performing the defining and reassigning steps two or more times.

20. An apparatus in accordance with claim 14, wherein reassigning comprises reassigning multiple interior pixel values.

5           21. An apparatus for altering the appearance of multiple images with differing line screen frequencies printed with a printer which is print sensitive to differing line screen frequencies, apparatus comprising:

          a raster image processor for converting the images into a digital bitmap comprised of an array of pixels wherein each pixel is assigned a digital value representing marking  
10   information;

          a rendering circuit for:

          defining each pixel as either a background pixel, interior pixel, one line pixel, or two line pixel; or an edge pixel;

          reassigning the digital value of the interior pixels to a fixed interior pixel  
15   value; and,

          reassigning the one line pixel, or two line pixel; or an edge pixel; independently so as to minimize print sensitivity to differing line screen frequencies between images.

20           22. An apparatus in accordance with claim 21, wherein the digital image data is binary.

23. An apparatus in accordance with claim 21, wherein the digital image data is a multi-bit.

24. An apparatus in accordance with claim 21, wherein reassigning comprises  
5 increasing the value of edge pixels with respect to interior pixels.

25. An apparatus in accordance with claim 21, wherein reassigning step comprises decreasing the value of edge pixels with respect to interior pixels.

10 26. An apparatus in accordance with claim 21, wherein the rendering circuit further comprises performing the defining and reassigning steps two or more times.

27. An apparatus in accordance with claim 21, wherein reassigning comprises reassigning multiple interior pixel values.

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